



The 2017 International Symposium on Electrical and Electronics Engineering (ISEE 2017)

November 29-30, 2017, Ho Chi Minh City, Vietnam

The ISEE 2017 is co-organized by Faculty of Electrical and Electronics Engineering, Ho Chi Minh City University of Technology, Vietnam; and Department of Electrical, Electronics and Information Engineering, Graduate School of Engineering, Nagaoka University of Technology, Japan. The ISEE 2017 will be held in Ho Chi Minh City University of Technology, Ho Chi Minh city, Vietnam on November 29-30, 2017. The aim of the conference is to provide a forum for regional and international researchers, educators, graduate students and engineers involved in the wide and multi-disciplinary areas of electrical and electronics engineering including microelectronics, communication systems, automation and control, and power and energy systems to disseminate their latest research findings and share their innovative ideas. This year, the conference is one of main activities towards the 60th Anniversary of HCMUT's Foundation. More information of ISEE 2017 can be found at: <http://feec-conf.com/isee2017>.

TOPICS OF INTEREST

Original contributions are sought in the wide and multi-disciplinary areas of electrical and electronics engineering including, but not being limited to:

Track 1: Microelectronic and Embedded Systems, covering topics on: Embedded System; Microelectronics; Digital and Analog Integrated Circuits and Systems; System Architecture, Simulation and Modelling; Microprocessors, ASICs and FPGAs; Real-Time and Embedded Systems; Optoelectronic materials and terahertz engineering.

Track 2: Communication Engineering and Systems, covering topics on: Optical Communications, Sensor networks, Mobile and Wireless Communications; Coding; Communications and Networking; Signal and Image Processing; Biomedical Imaging, Multimedia Systems and Applications; Microwave Engineering and Circuits; Antenna and propagation; Algorithms, Data technology, Applied harmonic analysis.

Track 3: Automation and Control, covering topics on: Approaches for Analysis and Design in Control Systems; Advanced Control Theory, Intelligent Control; Techniques for Dynamic Modeling and Identification in Control Systems; Computer Programs and Human-Machine Interfaces for Control Systems; Computer-Aided Control Systems Analysis and Design; Applications of Control Systems Technology; Networked Control Systems; SCADA; Computer Vision.

Track 4: Power and Energy Systems, covering topics on: Power Systems Operation and Control; Power Systems Planning and Management; SCADA, EMS and DSM; Power Quality & Harmonics; Power Electronics; Electrical Machines and Drives; Artificial Intelligent Applications in Power Engineering; New Technologies in Renewable Energy; Power Engineering Education; Power System Under Deregulation, Smart Grid.

Special Session: Advanced Technologies for IOT Applications.

Technical Workshop: Short-range access communications and related technologies (NICT – Japan)

SUBMISSION OF PAPERS

Prospective authors are invited to submit full papers of maximum 6 pages in PDF or Microsoft Word compatible file format. Templates of papers can be downloaded from the conference Website at: <http://feec-conf.com/isee2017/paper-submission.html>. All manuscripts will be peer-reviewed by the Technical Program Committee.

Accepted papers will be further reviewed for considerations to publish in a special issue of the Journal of Science and Technology Development, VNU-HCM Publishing House.